

QuickLogic CEO to Present at Silicon Summit East

SUNNYVALE, Calif., Oct. 5, 2018 /PRNewswire/ -- QuickLogic Corporation (NASDAQ: QUIK), a developer of ultra-low power multi-core voice enabled SoCs, embedded FPGA (eFPGA) IP, display bridge and programmable logic solutions, will be presenting, participating in a panel and demonstrating its <u>ArcticPro™ eFPGA</u> solutions at the Silicon Summit East 2018 in Saratoga Springs, NY.



Presentation:

Platform Approach for a Scalable Intelligent Edge Speaker: Brian Faith, President and CEO

October 9 at 1:00 p.m.

Panel:

Topic: Advanced Technology Solutions

Panelists: Brian Faith, QuickLogic's President and CEO will be joined by Albert Liu, Founder

and CEO of Kneron, Inc., Gary Bronner, Senior Vice President of Rambus Labs, and

Mukesh Khare, Vice President of IBM Research Semiconductor Group Moderator: Steve Teig, CTO & Senior Vice President, Xperi Corporation

Demonstration:

Booth 1

October 9, 2018 from 8:00 a.m. - 6:00 p.m.

The ArcticPro™ ultra-low power eFPGA architecture and mature software offer the ability to integrate programmable hardware accelerators to lower power consumption and add the flexibility to reconfigure a device's functionality in the field.

About QuickLogic

QuickLogic Corporation (NASDAQ: QUIK) enables OEMs to maximize battery life for highly differentiated, immersive user experiences with Smartphone, Wearable, Hearable and IoT devices. QuickLogic delivers these benefits through industry leading ultra-low power customer programmable SoC semiconductor solutions, embedded software, and algorithm solutions for always-on voice and sensor processing. The company's embedded FPGA initiative also enables SoC designers to easily implement post production changes, and increase revenue by providing hardware programmability to their end customers. For more information about QuickLogic, please visit www.quicklogic.com.

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