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QuickLogic Announces eFPGA Now Available on TSMC 40nm Process

- Embedded FPGA technology now available for SoCs on popular TSMC 40nm process

- Enables reprogrammable hardware acceleration and post-manufacturing feature changes for broad range of SoC designs

- eFPGA architecture and process node ideal for IoT endpoint and AI, security and automotive applications

SUNNYVALE, Calif., Nov. 15, 2018 /PRNewswire/ -- QuickLogic Corporation (NASDAQ: QUIK), a developer of ultra-low power multi-core voice enabled SoCs, embedded FPGA (eFPGA) IP, display bridge and programmable logic solutions, today announced that its ultra-low power ArcticPro™ eFPGA technology is now available on the popular TSMC 40nm process node. The embedded IP is fully supported by a complete development environment, which is also immediately available, and enables designers of IoT endpoint, AI, automotive and security applications to add hardware acceleration capabilities and post-manufacturing design flexibility to their SoC platform devices.

TSMC was the first foundry to mass produce products for multiple customers on a 40nm process when it was introduced, and it has since become a standard for SoC designs addressing a wide range of applications due to its attractive combination of performance, low power consumption, and cost, making it an excellent fit for the ultra-low power consumption eFPGA technology from QuickLogic.

SoC designs targeting IoT endpoint, AI, security and automotive applications can exploit the flexibility inherent in eFPGA technology by using it to address fragmented markets, new or emerging standards, market adjacencies or new competitive threats. SoC developers can be comfortable integrating eFPGA technology knowing that QuickLogic has decades of experience with FPGA architectures, technology, software and IP, and has worked successfully with TSMC for many years.

"TSMC's 40nm process is an excellent choice for integrating eFPGA technology, and is widely used in SoCs that require very low standby power and non-volatile flash memory," said Mao Wang, product marketing director at QuickLogic Corporation. "The popularity of that process node means that many more SoC designers can now exploit the many

advantages of embedding programmable logic into their platforms."

Availability

QuickLogic's ArcticPro eFPGA technology on TSMC's 40nm process, and its associated software design suite, is available now for evaluation. For more information, please visit www.quicklogic.com/technologies/efpga-ip/arcticpro-efpga/

About QuickLogic
QuickLogic Corporation (NASDAQ: QUIK) enables OEMs to maximize battery life for highly differentiated, immersive user experiences with Smartphone, Wearable, Hearables and IoT devices. QuickLogic delivers these benefits through industry leading ultra-low power customer programmable SoC semiconductor solutions, embedded software, and algorithm solutions for always-on voice and sensor processing. The company's embedded FPGA initiative also enables SoC designers to easily implement post production changes and increase revenue by providing hardware programmability to their end customers. For more information about QuickLogic, please visit www.quicklogic.com and http://blog.quicklogic.com.

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